

Electronic Acknowledgement Receipt

EFS ID:	4835594
Application Number:	10534962
International Application Number:	
Confirmation Number:	1696
Title of Invention:	Circuit board, multi-layer wiring boards, method of producing circuit boards and method of producing multilayer wiring boards
First Named Inventor/Applicant Name:	Masayoshi Kondo
Customer Number:	25461
Filer:	Robert G. Weilacher
Filer Authorized By:	
Attorney Docket Number:	033036.088
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Application Type:	U.S. National Stage under 35 USC 371

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
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1		33036-088.PDF	208316 890da05d7bb5ed34add4723669b88e8dfc45ac9d	yes	15
	Multipart Description/PDF files in .zip description				
	Document Description		Start	End	
	Request for Continued Examination (RCE)		1	1	
	Amendment Submitted/Entered with Filing of CPA/RCE		2	14	
	Extension of Time		15	15	
Warnings:					
Information:					
2	Fee Worksheet (PTO-06)	fee-info.pdf	32005 bca8fba318e75a166fc6a865a107ce486107abd3	no	2
Warnings:					
Information:					
Total Files Size (in bytes):			240321		
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